

Strain free GaAs grown on Si by MOVPE

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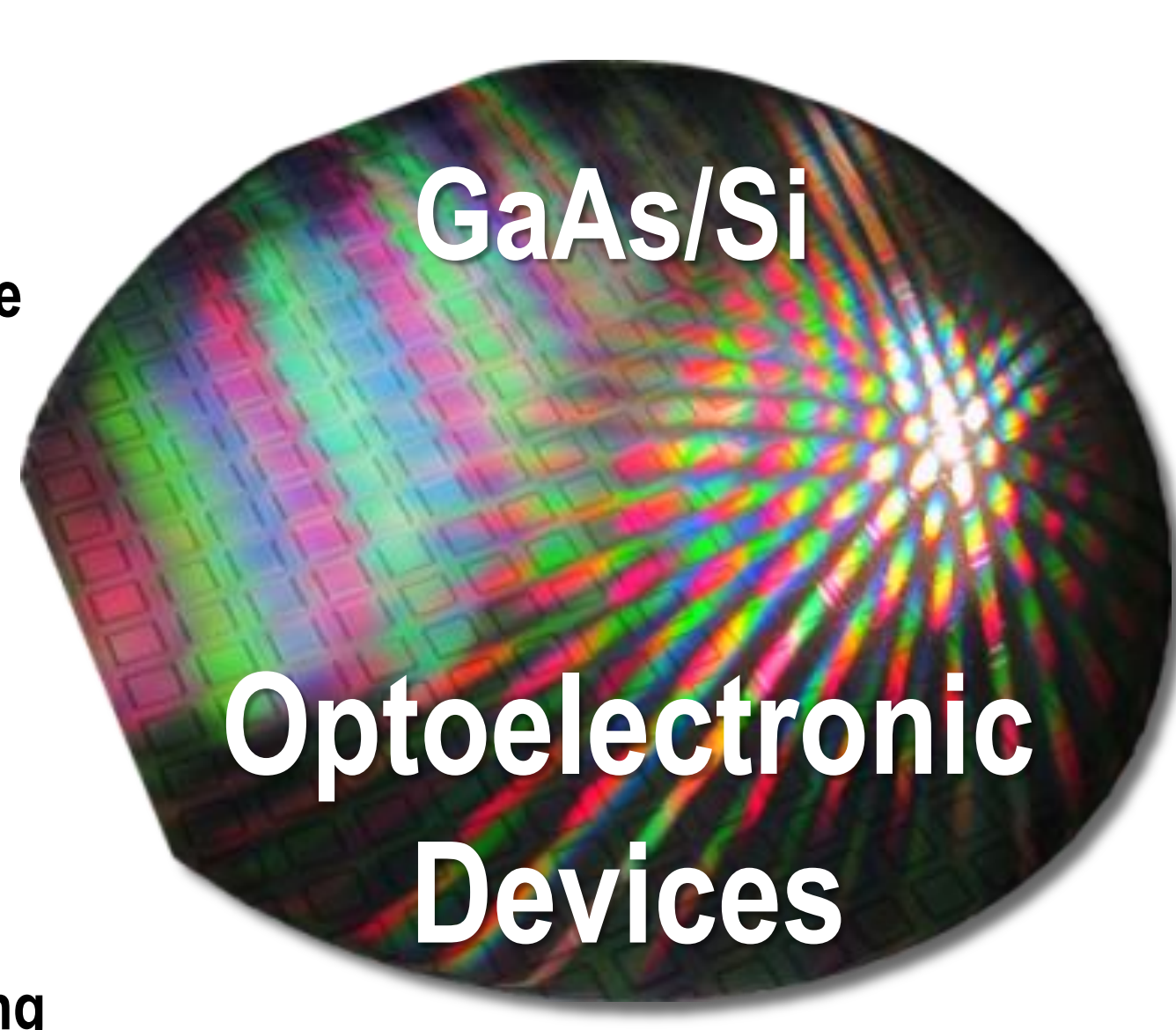
Motivation: III-V integration on Si

Si

Standard in production
Abundant and inexpensive
Available in large size

III-V

Larger electron mobility
Direct bandgap
Flexible bandgap engineering



GaAs/Si
Optoelectronic Devices

Challenges

Thermal expansion mismatch (wafer bowing & cracks)
 $\alpha_{\text{GaAs}} > \alpha_{\text{Si}} \Rightarrow$

Lattice mismatch (high TDD)
 $a_{\text{GaAs}} > a_{\text{Si}} \Rightarrow$ TDs, MDs

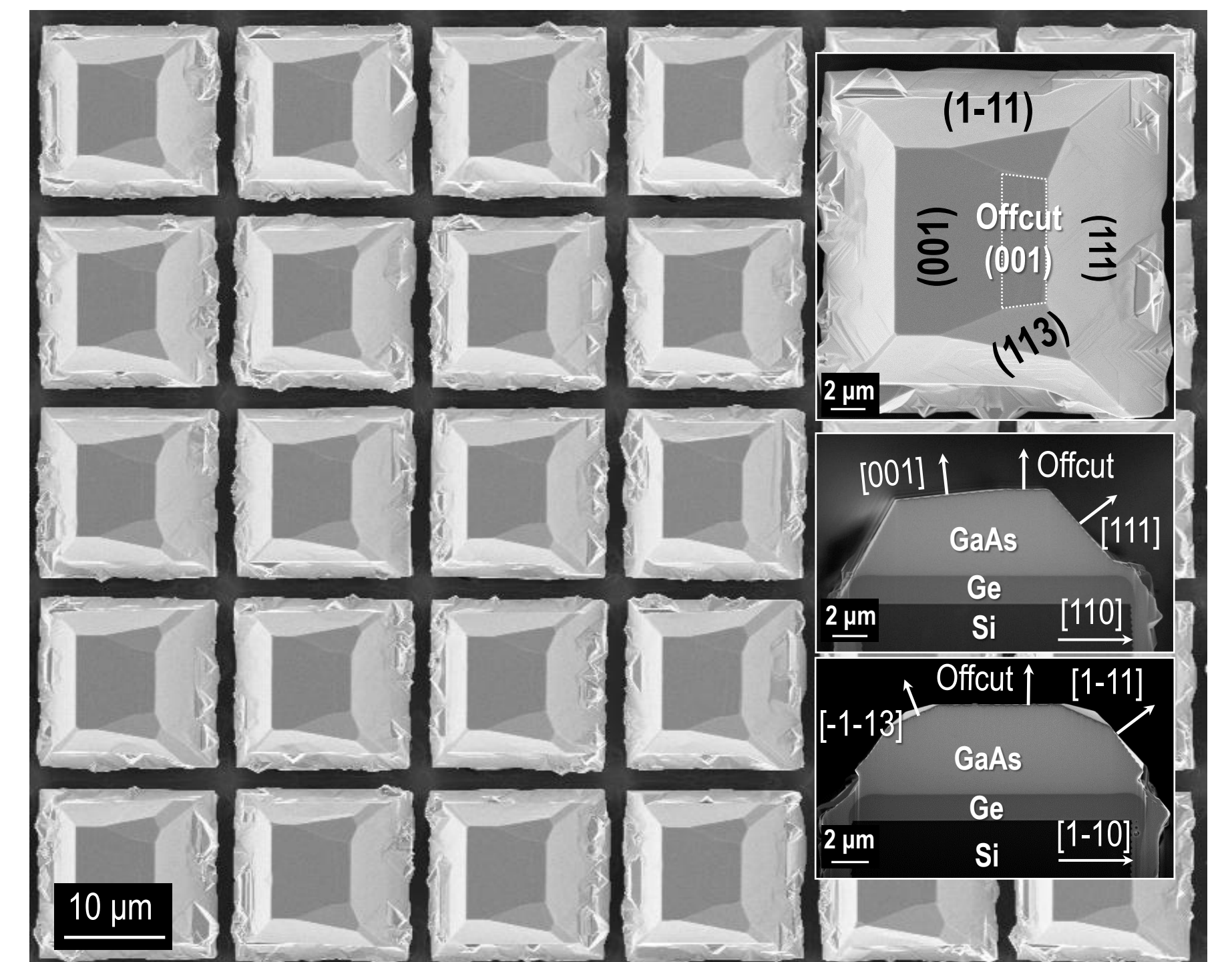
Anti Phase domains

GaAs crystals grown by MOVPE on Ge/Si patterns

GaAs MOVPE growth on Ge/Si towers



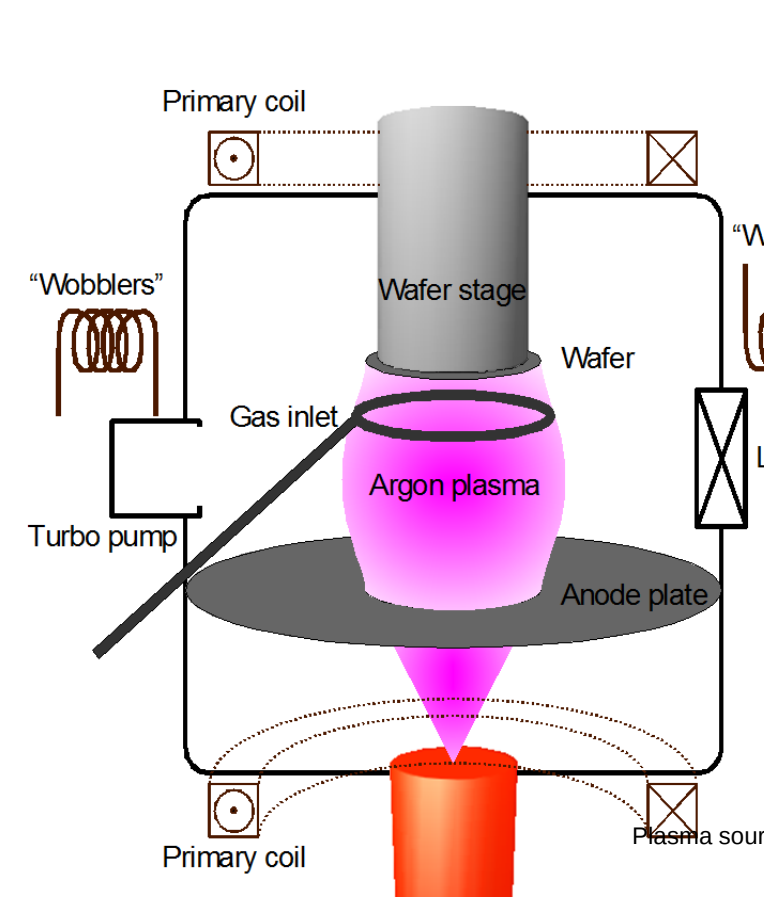
Two temperature growth method
- Low Temperature nucleation ($T=500^\circ\text{C}$)
- GaAs growth at $T=680^\circ\text{C}$, $V/III=50$

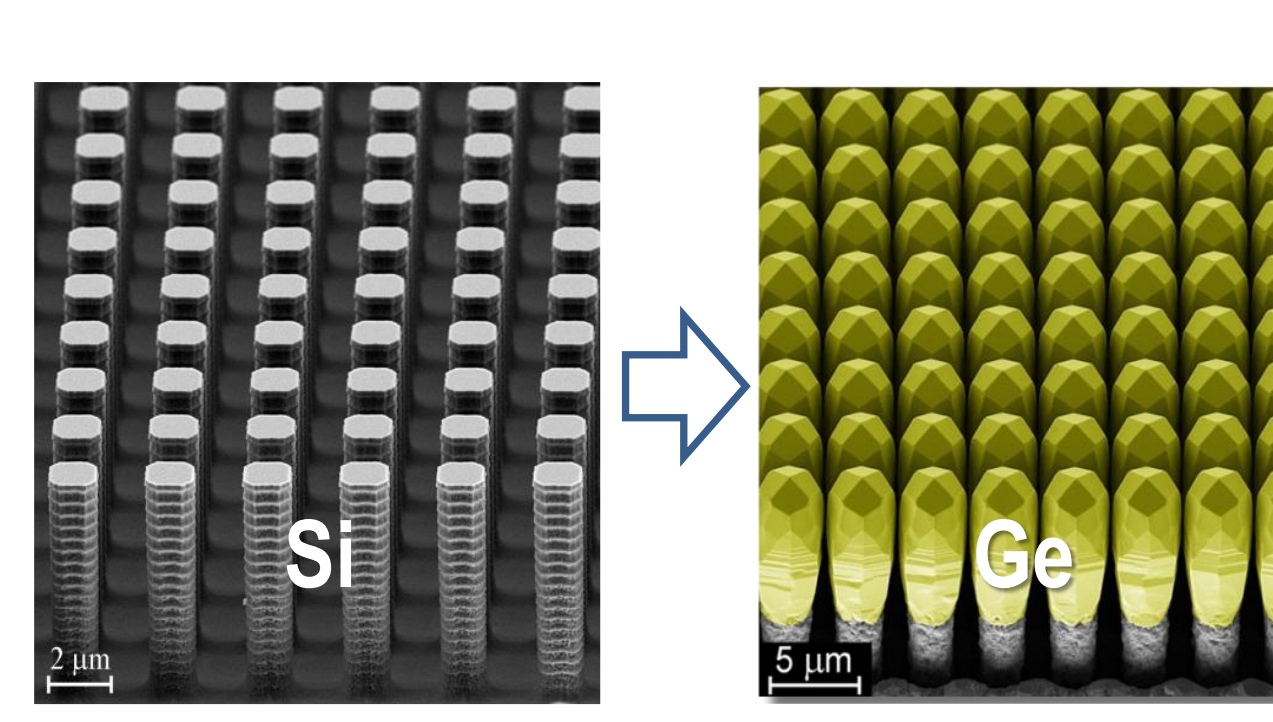


Background: 3D epitaxy

LEPECVD

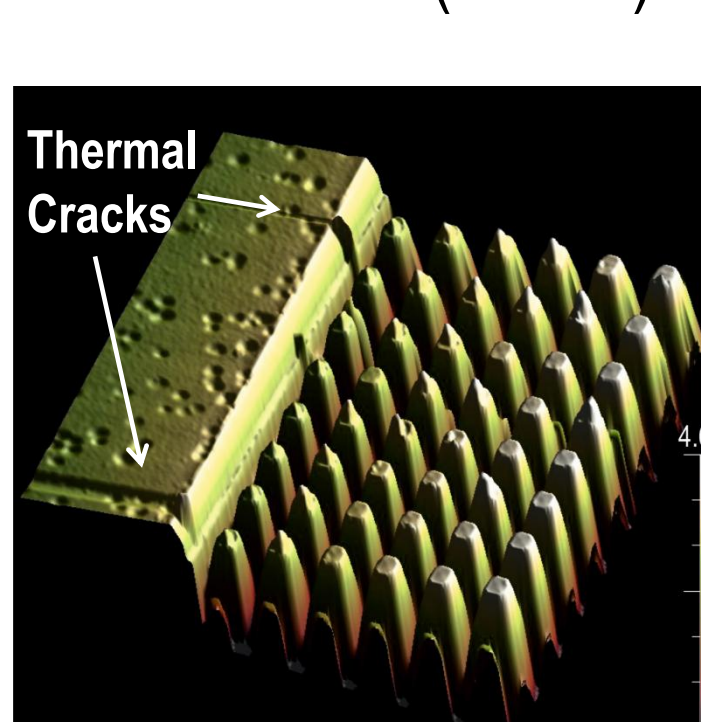
Low substrate temperature $\sim 500^\circ\text{C}$
High growth rates (10 nm/s)





Si Ge

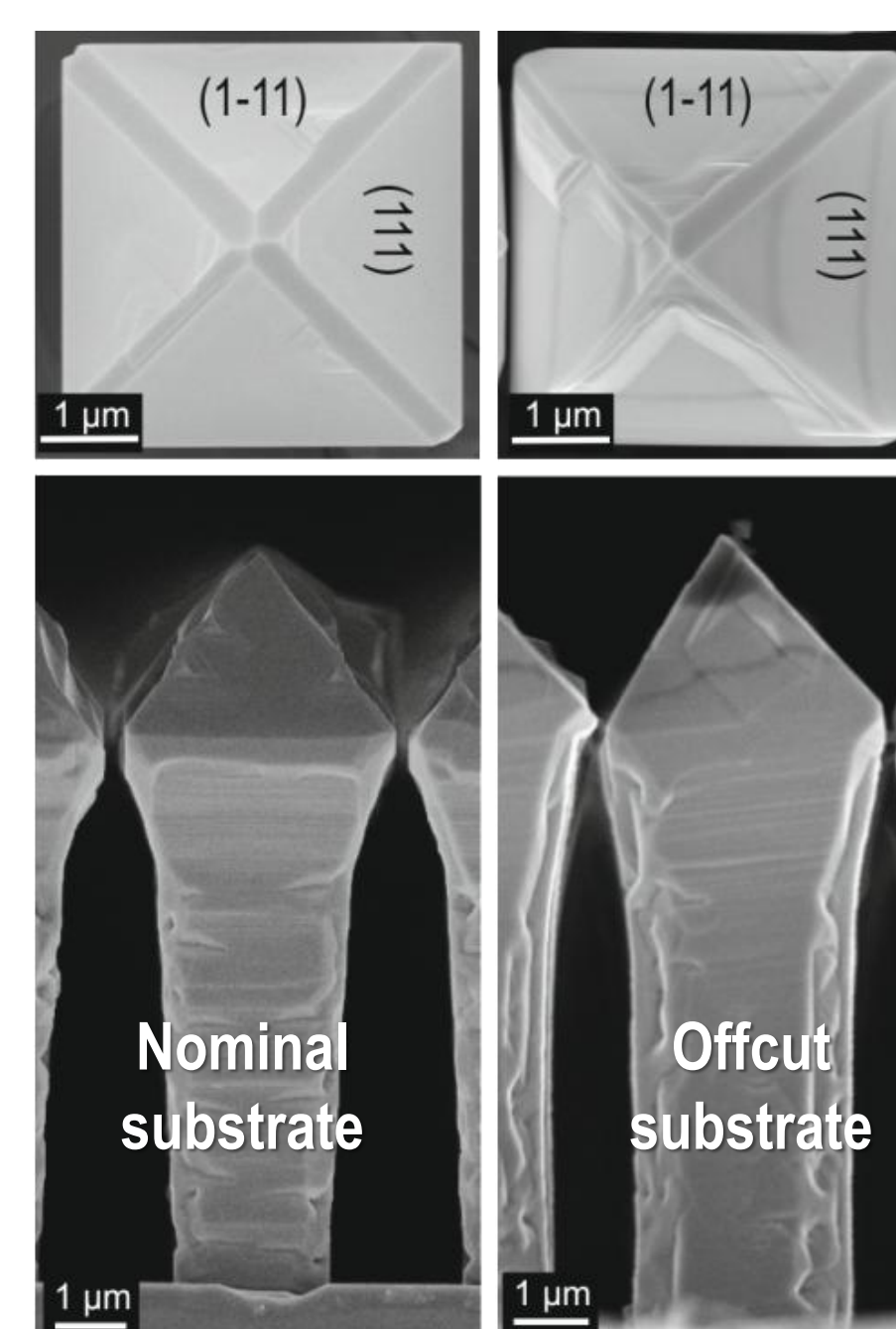
- ART + faceting:
- TDD zero on faceted material
- Self limited lateral growth: Full coverage of wafer surface ($\sim 98\%$)



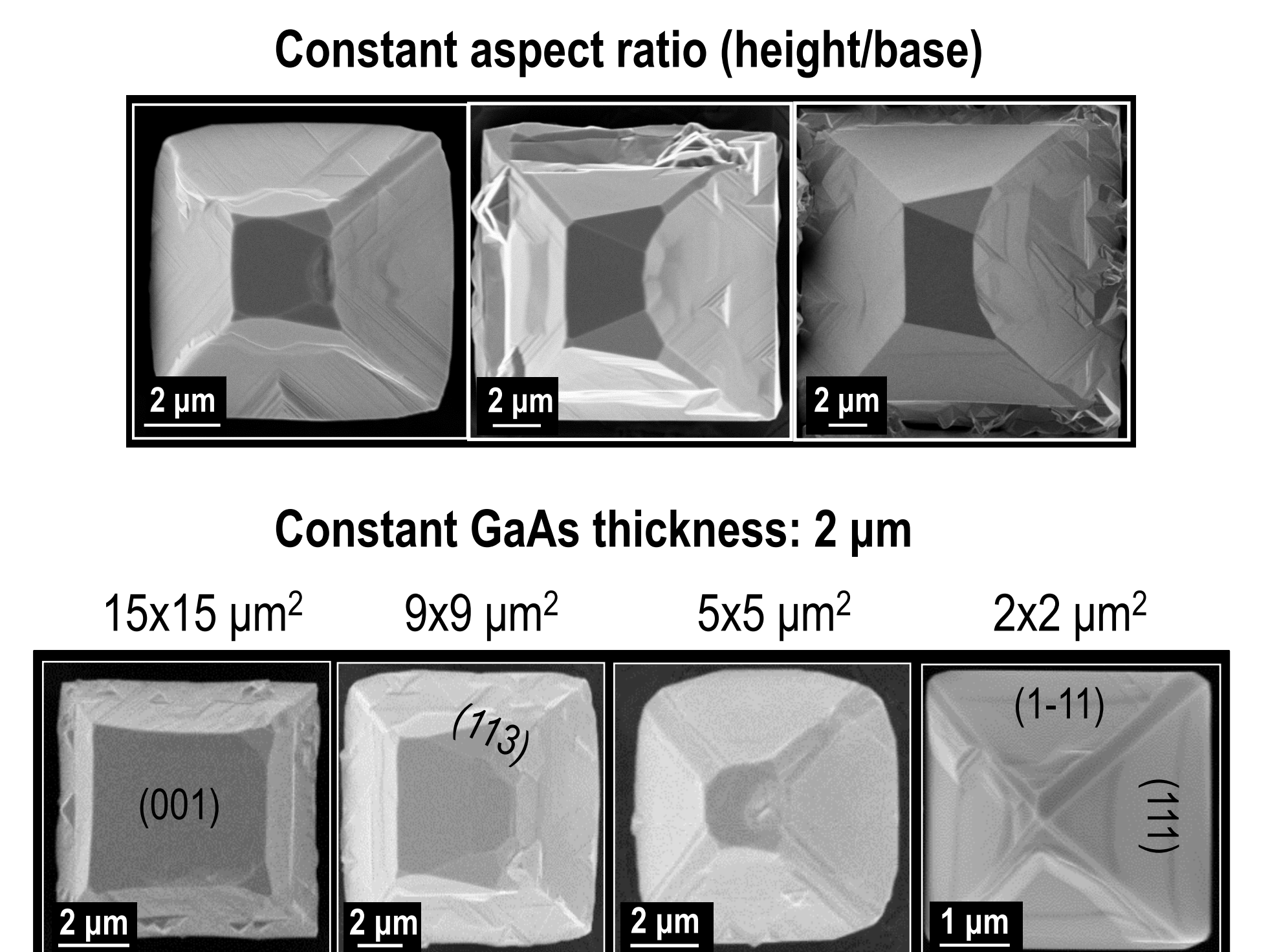
Thermal Cracks

Is it possible to extend this growth method to other materials/systems?

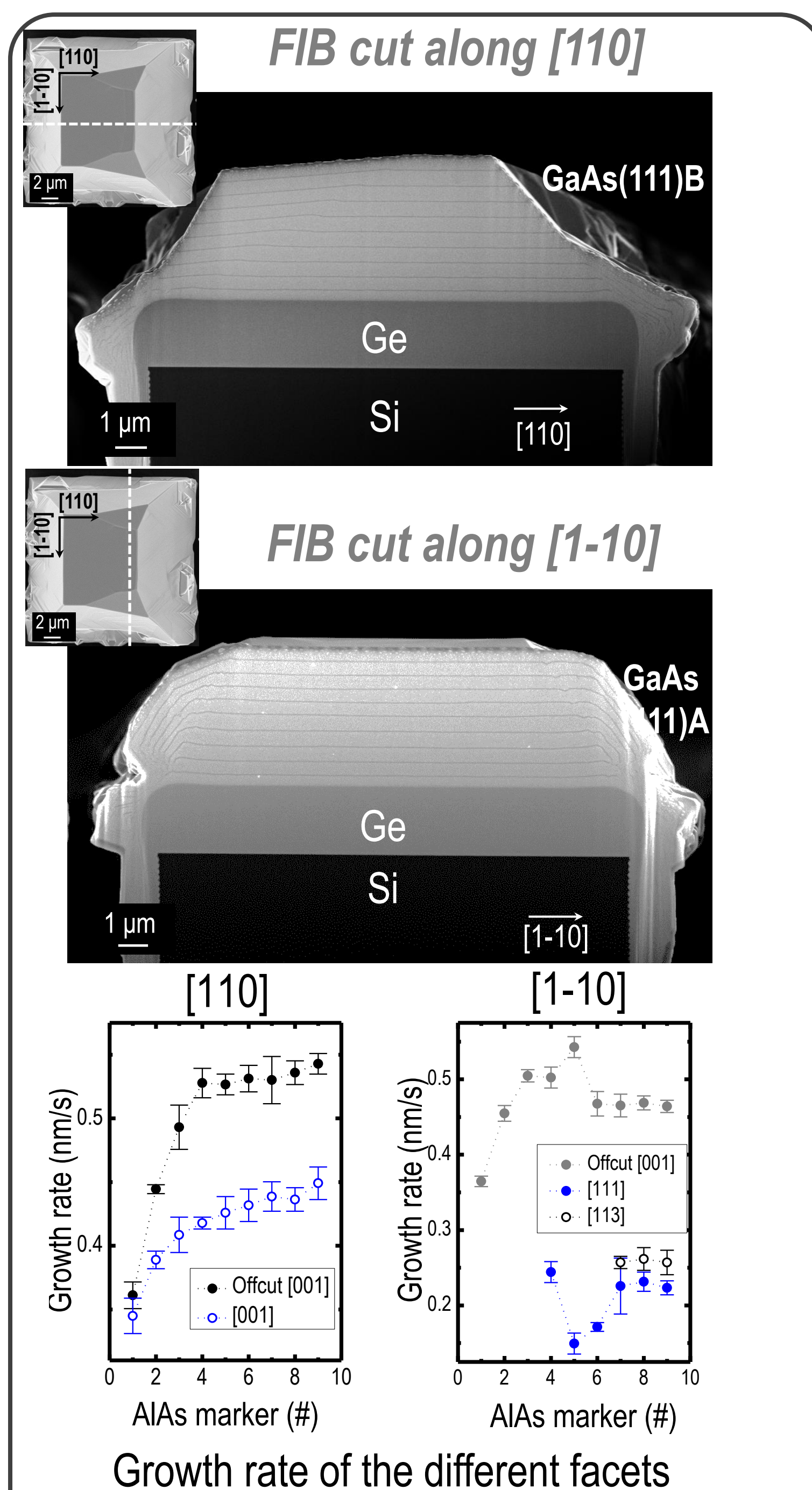
GaAs crystals morphology: substrate orientation



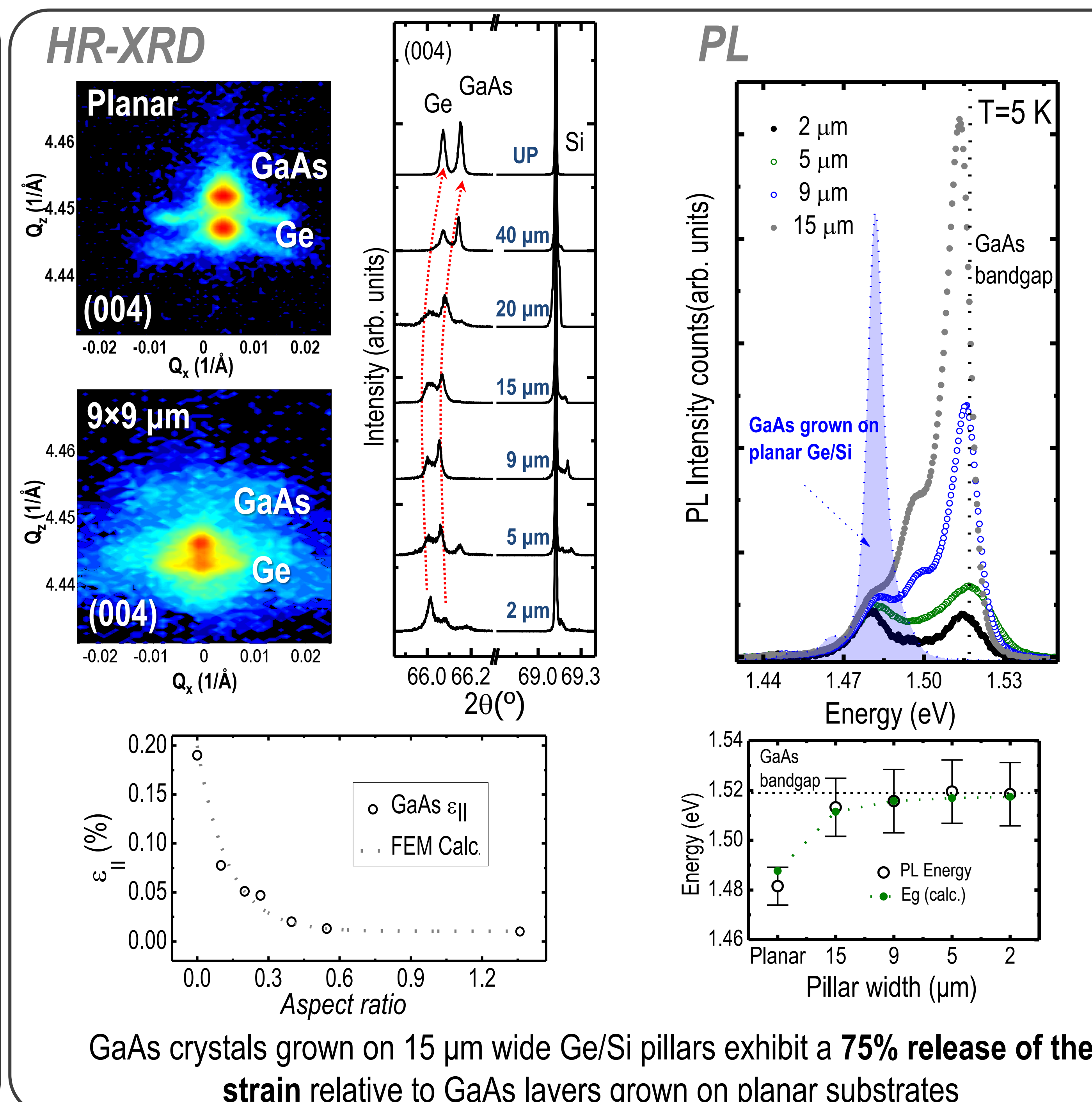
GaAs crystals morphology: substrate aspect ratio



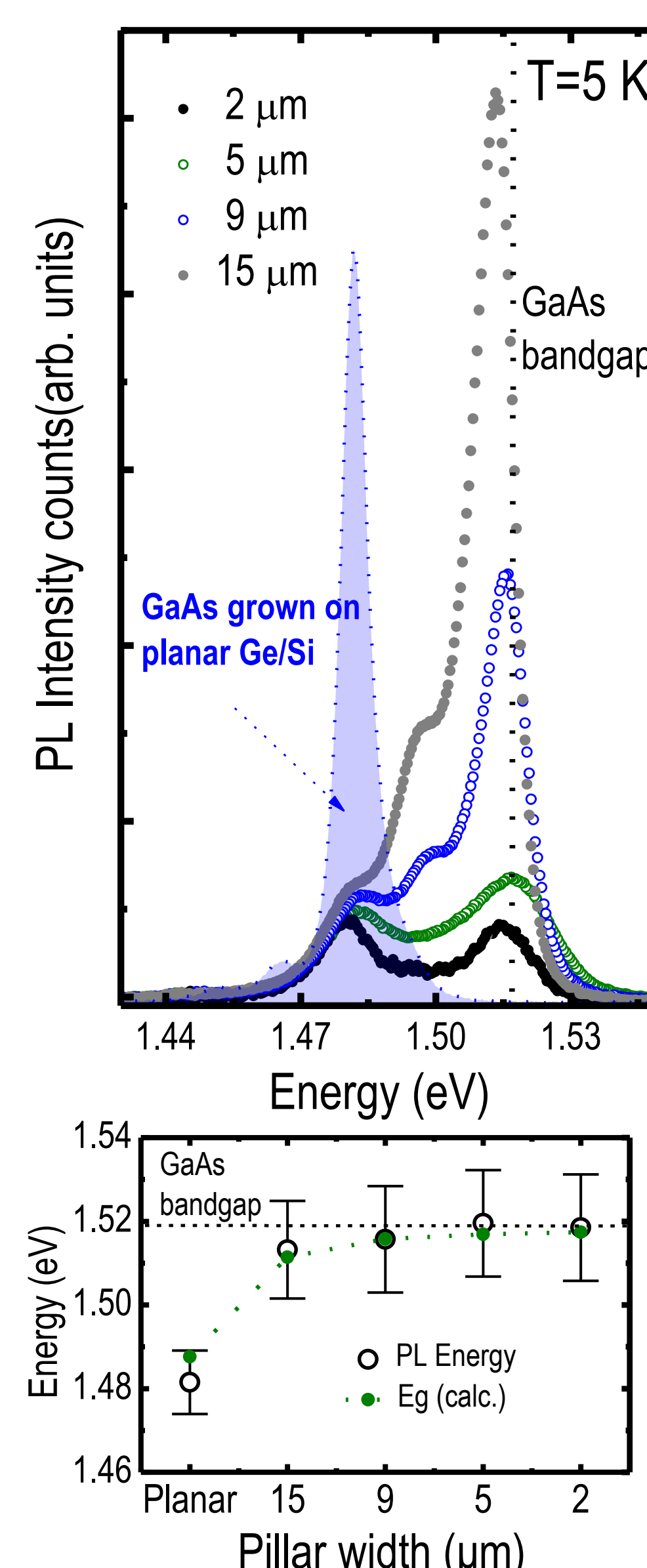
Growth kinetics



Strain free GaAs

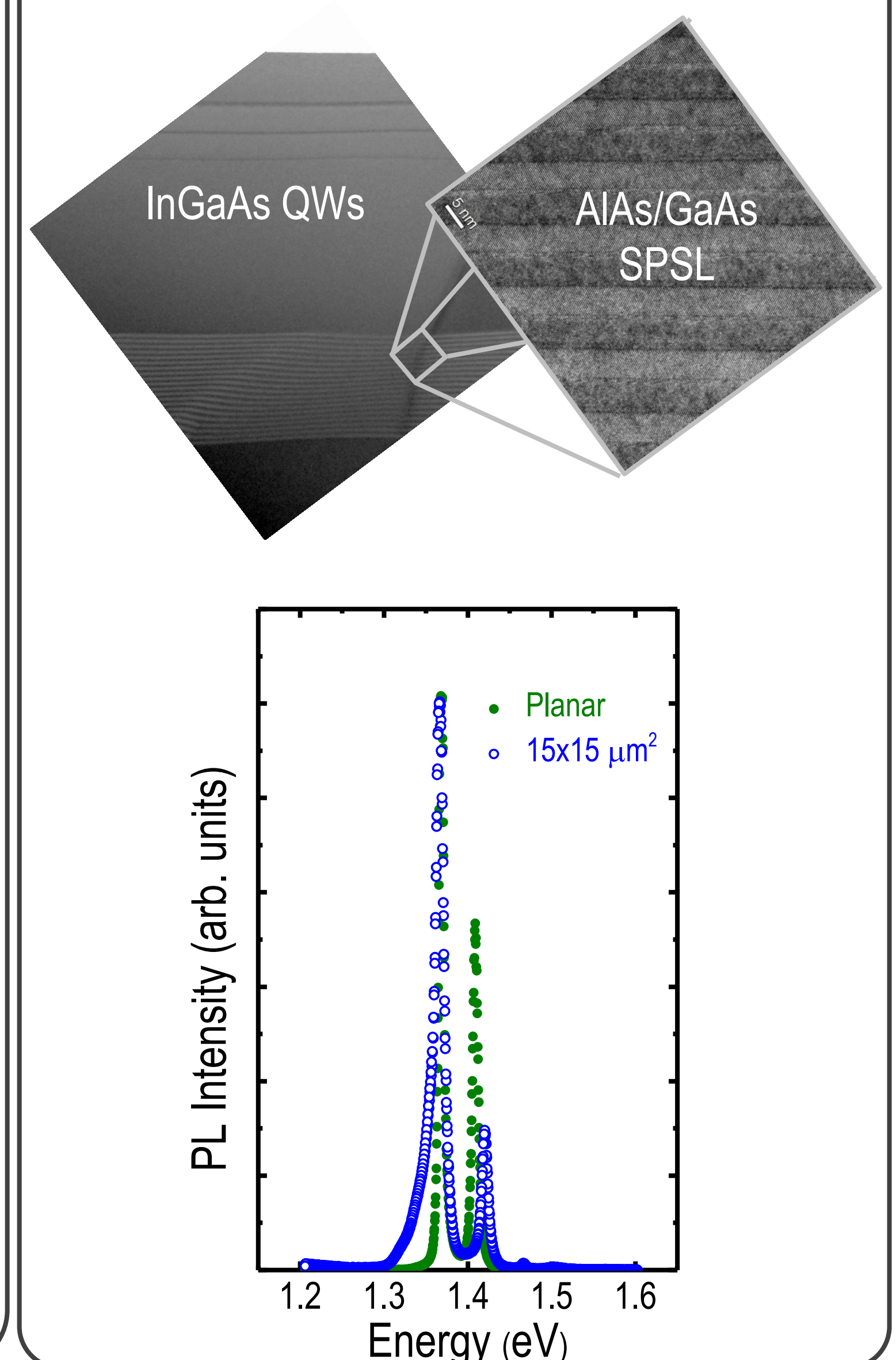


PL



InGaAs/GaAs QWs on Si

Successful integration of optically active quantum structures on Si



Acknowledgements

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References

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- M. Richter, E. Uccelli, A.G. Taboada, D. Caimi, N. Daix, M. Sousa, C. Marchiori, H. Siegart, C.V. Falub, H. von Känel, F. Isa, G. Isella, A. Pezous, A. Dommann, P. Niedermann, J. Fompeyrine, Journal of Crystal Growth In press (2013)