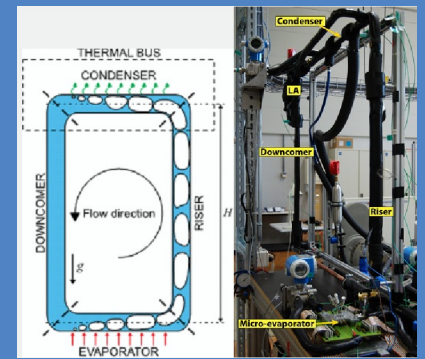




YINS

ENERGY- AND THERMAL-AWARE DESIGN OF MANY-CORE
HETEROGENEOUS DATACENTERS

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What it's about...

Developing new technologies and optimization methodologies to develop next-generation energy-efficient datacenters.

Context and project goals

Energy efficiency in datacenters is of strategic importance to Switzerland, as over 75% of the Swiss economy is service-based and depends on information technology (IT), which makes Switzerland one of top spenders of IT per capita. IT is also witnessing a major paradigm shift towards Cloud Computing with datacenters emerging as a key backbone of services at scale, making energy dissipated in datacenters a key concern even with moderate increases in IT electricity demands. In particular, because of Switzerland's decision to abandon nuclear energy by 2034, improvements in energy efficiency are imperative to make up for half of the electricity otherwise furnished by nuclear power. Meanwhile, in Switzerland many IT departments for enterprises, research, and governmental organizations alike are at capacity with regards to their electricity budget. Thus, the only feasible solution to achieve energy sustainability is to drastically increase the power efficiency of data centers.

The design of datacenters today is a really complex process, where important opportunities exist both within and across various server and infrastructure components. Server software, system and silicon technologies as well as infrastructure for cooling and power delivery have historically been designed in isolation with an over provisioning of resources to guarantee a desired quality of service. Unfortunately, due to the diverse nature of workloads and demands on resources, such over provisioning results in prohibitive levels of waste in energy and efficiency. Modern volume server software and hardware is broadly based on designs primarily derived from the desktop market and are ill-suited for serviced-oriented server workloads. Similarly, while most datacenters make use of air-cooling technologies to ensure the correct running of the servers, air-cooling is reaching fundamental physical limits in efficiency with a continued increase in server density requiring innovation in cooling technologies.

In YINS, we propose to develop a radically new thermal-aware design approach for next generation energy-efficient datacenters. This new design approach tightly integrates the cooling infrastructure definition with holistic system-level power, performance and thermal management. This vertically-integrated system-level management paradigm goes beyond hardware and software boundaries by redesigning the entire datacenter to maximize performance given a target power, area and cost budget. Therefore, we propose to develop new server technologies based on Fully Depleted Silicon On Insulator (FDSOI) and specialized server architectures. Finally, these novel architectures interact with new on-chip microfluidic cooling delivery at server-level and passive thermosyphon cooling systems for the rack and room-level, as well as energy recovery strategies for the complete datacenter.

To realize its vision, YINS requires inter-disciplinary research at the boundaries of multiple scientific domains, as well as developing and integrating innovations in critical research areas, namely, computer systems, circuits and semiconductor technologies, and cooling technologies, large-scale simulation, software synthesis and optimization, statistical network modeling and model predictive control theory. Therefore, the YINS consortium includes six world-renown academic partners from EPFL and ETHZ covering key research areas of computer, electrical and mechanical engineering, and three key industrial partners in Switzerland for datacenter design and large-scale IT banking services provisioning (Credit Suisse, Eaton and BrainServe), and three third-party industrial partners (Constellium, FrioTherm and Osmobleue).

How it differentiates from similar projects in the field

The vertically-integrated system-level management paradigm that YINS targets for datacenters goes beyond competitors in the field by performing inter-disciplinary research at the boundaries of multiple scientific domains.

It integrates innovations in several research areas, namely, computer engineering and cooling design, large-scale computing system simulation, software generation and optimization, statistical network modeling and model predictive control theory.

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Quick summary of the project status

The team completed the characterization (energy, performance and temperature) full HW IPs synthesized and manufactured in 28nm FDSOI technology tailored to emulate parts of parallel accelerators for HPC and many-core server systems.

The project developed PowerCool, the first-of-its-kind mathematical model that can be used to explore architectures of micro-scale on-chip microfluidic fuel cell networks for joint cooling and localized power generation and delivery for memories and logic of many-core servers.

A new family of designs was developed for dedicated many-core network interfaces that can offload virtual switching functionality and trade-off low latency vs. high bandwidth according to different data center requirements.

A novel stacked-DRAM cache design, Unison Cache, was introduced, which includes the tag metadata directly into the stacked DRAM to enable scalability to arbitrary stacked-DRAM capacities.

An extensive experimental campaign of server thermal behaviors was performed (uniform and non-uniform heat fluxes by mimicking multi-core microprocessors) applied on a two-phase multi-microchannel evaporator under steady-state and transient regimes, and proving the high thermal (energy) performance and safe operation of such device for challenging applications.

Simulations of an entire thermosyphon cooling loop implementation and preliminary experiments in a new test bench were developed.

The team developed a flow to extract complete system thermal models of a target processor without requiring any hard-to-get information, such as, the detailed processor floorplan or system power traces. They validated it against an Intel Xeon 8-core server processor.

Success stories

Within YINS, IIS-ETHZ members presented a new method to estimate thermal profiles in many-core servers using directly on-line feedback from real sensors that are dynamically calibrated in many-core computing architectures. This approach was validated using the Intel SCC many-core processor and this work was awarded the Best Paper Award at DATE 2014.

The new system developed in YINS to monitor wirelessly energy consumption, temperature and humidity in racks and servers, called Power Monitor System and Management (PMSM), was transferred to the Wispes Sàrl start-up to start commercializing it in a short term (less than a year) in the area of energy-efficient datacenter monitoring, and it is in the process of consolidating a maintenance and extra purchase contract to expand its use in Credit Suisse datacenters in the complete Switzerland.

Presence in the media:

The “Power System Monitoring and Management (PMSM) device and system, developed at ESL-EPFL in cooperation with Credit Suisse, has been cited in the Data Center Dynamics International Magazine.

Main publications

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